



Typical Features

- ◆ Fixed input voltage, isolated & unregulated, output power 1W
- ◆ Efficiency 83% (Typ.)
- ◆ Mini SIP packaging
- ◆ Isolation Voltage 1500VDC
- ◆ Operating Temperature from -40 °C to +105 °C
- ◆ Plastic case, flame class UL94 V-0



Test Condition: Unless otherwise specified, all parameter values had been tested at nominal input voltage, pure resistive rated load, and at room temperature 25°C.

Application Field

This series of products can be widely used in the fields of instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typic	al Product List										
Certificate	Part No.		Voltage e (VDC)		it Voltage/ nt (Vo/Io)	Input Current (mA) Typ. Nominal Volt.		Max. Capacitive Load	Ripple & Noise① (20MHz) (mVp-p)	(%) (load/	iency @full /nom. tage
Φ		Nom.	Range	Vo (VDC)	lo(mA) Max/Min	Full load	No Load	(uF)	Max/Typ.	Min	Тур.
-	FN1-05D15BNA	5	4.5 - 5.5	±15	±34/±4	225	18	220	100/60	80	83

Note ① The ripple and noise are tested by the twisted pair method.

Input Specifications							
Item	Operating conditions	Min.	Тур.	Max.	Unit		
Input inrush voltage (1Second.max.)	5Vdc Input	-0.7		9	VDC		
Input Filter	Capacitor Filter						
Hot Plug	Unavailable						

Output Specifications							
Item	Operating conditions	Min.	Тур.	Max.	Unit		
Output Power		0.1		1	W		
Output Voltage Accuracy	Please refer to the output voltage deviation curve (Figure 1)						
Load Regulation	10% - 100% load		10	15	%		
Line Voltage Regulation	Input Voltage Change ±1%			1.2			
Temperature Drift Coefficient	100% Load			±0.03	%/°C		
Short Circuit Protection	Continuous, self-recovery						

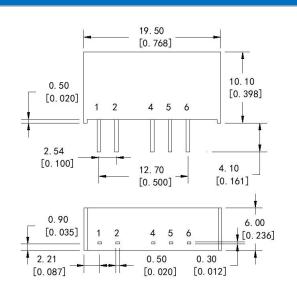


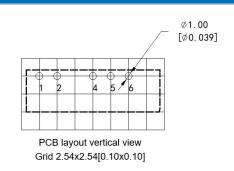


General Specifications							
Item	Operating condition	s	Min.	Тур.		Мах.	Unit
Switching Frequency	Nominal input voltage, full load			260			KHz
Operating Temperature	Please refer to the temperature derating curve (Figure 2)		-40			+105	
Storage Temperature			-55			+125	°C
Case temperature rise	Operating at Ta =25℃			30°			
Pin Soldering Temperature	1.5mm from the case, 10	S				300	
Relative humidity	No condensation		5			95	%RH
Isolation Voltage	I/P-O/P, test 1min, leakage current <1mA		1500				VDC
Insulation Resistance	Input-Output, @ 500VDC		1000				ΜΩ
Isolation Capacitor	Input/Output,100KHz/0.1	V		20			pF
Vibration			10-150Hz, 5G, 30 Min. along X, Y and Z			and Z	
MTBF	MIL-HDBK-217F@25°C	!	3500				K hours
Case Material	Pl	astic in Bla	ck, flame class	UL94 V-0)		
Unit Weight	2.1 g (Typ.)						
Cooling Method		Natural air					
5 1:	Tube size (525*18*10mm)		25PCS/Tube				
Packing	Carton size (542*110*155r	2000PCS/Carton (Total 80 Tubes)					
Unit dimensions	LxWxH	19.50×	0.768 × 0.236 × 0.398 inch			0.398 inch	

EMC Performance					
CE CISPR32/EN55032 CLASS B (with Recommended EMC circuit)		CISPR32/EN55032 CLASS B (with Recommended EMC circuit)			
EMI	RE	CISPR32/EN55032 CLASS B (with Recommended EMC circuit)			
EMS	ESD	IEC/EN61000-4-2 Air±8kV, Contact±6kV perf.Criteria B			

Mechanical Dimensions





Unit: mm[inch]
Pin section tolerance ±0.10[±0.004]
General tolerance ±0.50[±0.020]



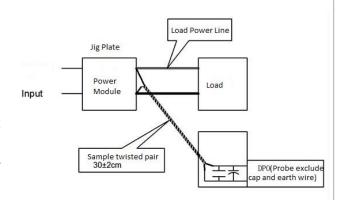


Pin Function definition						
Pin No.	1	2	3	4	5	6
Dual output	+Vin	GND(Input)	No Pin	-Vout	СОМ	+Vout

Note - Please take the pin definition on the product label as the right one if there is any difference between the data sheet and the one printed on the product label.

Ripple & Noise Test Instructions (Twisted Pair Method, 20MHz Bandwidth)

- 1) The Ripple & noise test needs 12# twisted pair cables, an oscilloscope which bandwidth should be set to 20MHz, 0.1uF polypropylene capacitor and 10uF high-frequency low-resistance electrolytic capacitor are connected in parallel with the probes (100M bandwidth). The oscilloscope should be set at the Sample Mode.
- 2) The test diagram is shown on the right. The converter output connects to the electronic load by the jig with cables which size should be defined according to the output current value. The twisted pair (length 30cm±2 cm) should be connected in parallel with the load, the location is as close as possible to the output pins or terminals. The test can be started after input power on.



Product Performance Curves

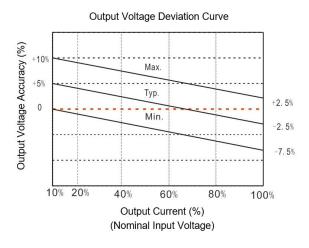


Figure 1

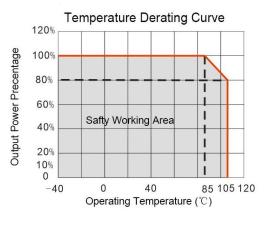


Figure 2





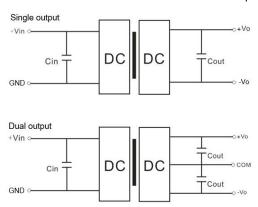
Recommended Circuits for Application

1. Requirement for Output load

- a. To ensure the converter operates efficiently and reliably, its minimum load should not be less than 10% of the rated load. It is recommended to connect a resistor in parallel at the output when the real load is less than 10% (the sum of the power consumed should be equal to or bigger than 10% of the rated power).
- b. The maximum capacitive load is tested at nominal input voltage and full load. The converter may not start or be damaged at the capacitive over-load.

2. Typical application circuit

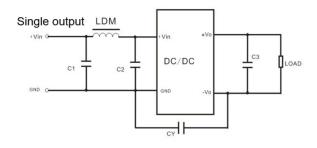
To ensure effectively decrease the input and output ripple and noise, a capacitor filter can be connected at the input and output, the application circuit is shown in the figure below. The suitable filter capacitors should be chosen as the recommended capacitive load values in Table 1. The converter could not start if the capacitance is too big.



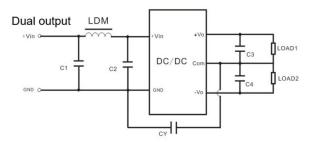
Recommended Capacitive Load Value Table (Table 1)

Vin (Vdc)	Cin	Single Vout (Vdc)	Cout (µF)	Dual Vout (Vdc)	Cout (μF)
5	10 μ F/16V	3. 3	10 µ F/16V	± 3.3	4. 7 μF/16V
12	2. 2 µ F/25V	5	10 µ F/16V	±5	4.7µF/16V
15	2. 2 µ F/25V	9	2. 2 µ F/25V	±9	2. 2 µ F/25V
24	1 μ F/50V	12	2. 2 µ F/25V	±12	1 µF/25V
		15	1 μ F/25V	±15	1μF/16V
		24	1 μF/50V	±24	0. 47 µ F/50

3. Recommended EMC Circuit



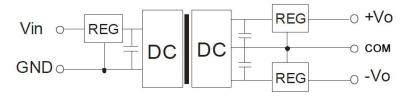
Input v	oltage	5VDC	12/15/24VDC	
	C1/C2	4. 7μF/16V	4. 7μF/50V	
- N.	CY	270 pF	270 pF	
EMI	C3	Refer to Cou	it in Table 1	
	LDM	6.8 µ H	6.8 µ H	



Input v	oltage/	5VDC	12/15/24VDC		
	C1/C2	4. 7μF/16V	4. 7μF/50V		
EMI	CY	270 pF	270 pF		
EIVII	C3/C4	Refer to Cou	ut in Table 1		
	LDM	6.8 µ H	6.8 µ H		

4. Output voltage regulation and overvoltage protection

The simple solution to achieve the output regulated voltage, over voltage and over current protections is to connect a linear regulator with overheat protection at input or output, and a capacitor filter connected in parallel as below circuit. Filter capacitive value recommended see table 1, Linear regulator should be chosen according to the actual voltage & current for operating. Or Aipu NW series products are recommended instead.







Application Notice

- 1. This series of converters should not be used in parallel, and they do not support hot-plugging.
- 2. The product performance in this datasheet cannot be guaranteed if it works at a lower load than the minimum load condition.
- 3. All values or indicators in this datasheet had been tested based on Aipupower test specifications.

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